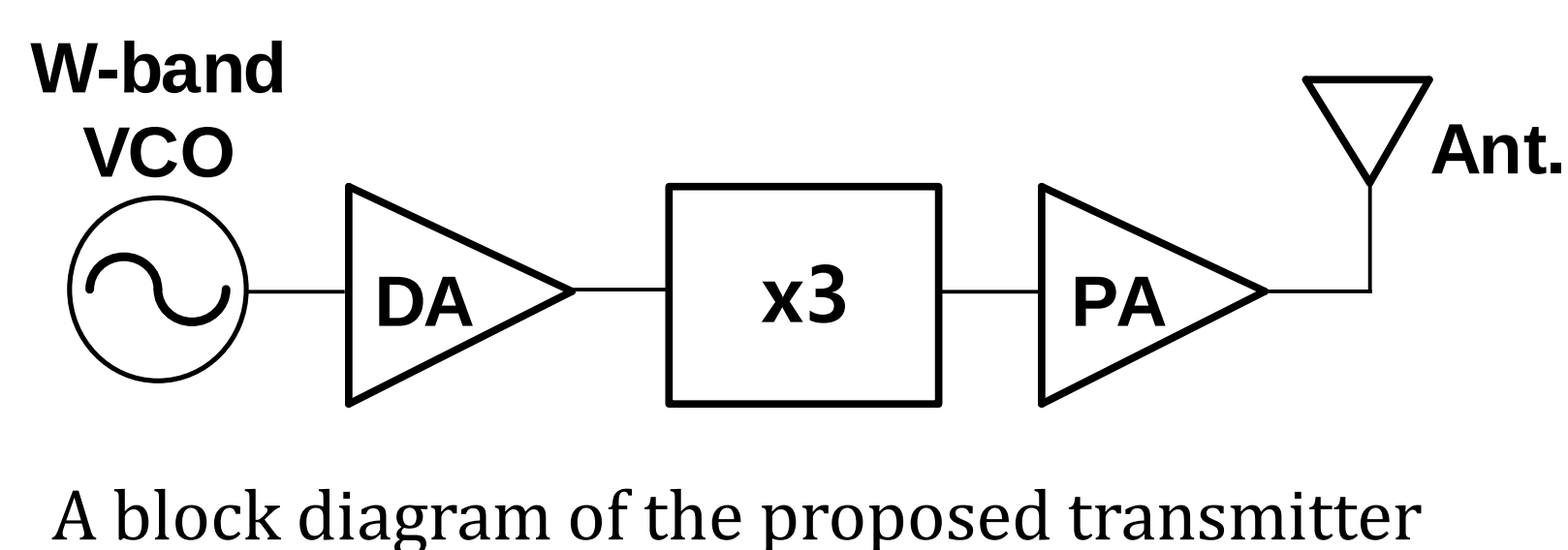


# A +3dBm-EIRP 240-GHz Circular-Polarized Radiator Utilizing a Sub-THz PA in 65-nm CMOS

## Introduction

- ❖ Common techniques to boost equivalent isotropically radiated power (EIRP) of a sub-terahertz radiator:
  - Using array of unit radiators
  - Using fundamental sub-terahertz amplifier
  - Using lens to focus the radiated beam
- ❖ This work presents a 240 GHz radiator which has block diagram as bellow
- ❖ By optimize the 240 GHz PA, a good output power is attained by using only single radiating cell



## Radiator Design

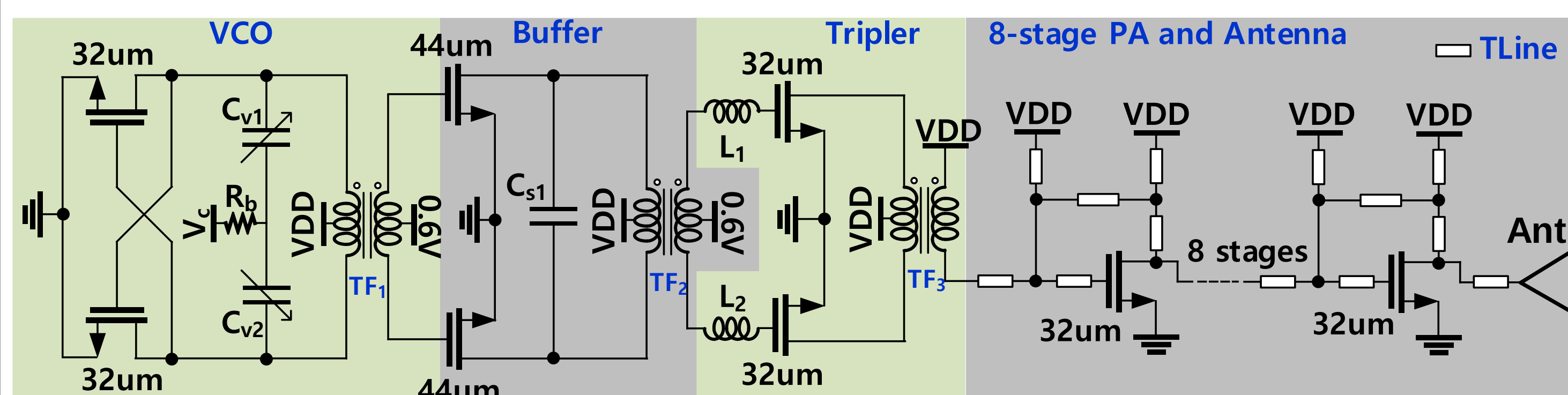


Fig. 1. Schematic of the propose inductive feedback push-pull power amplifier.

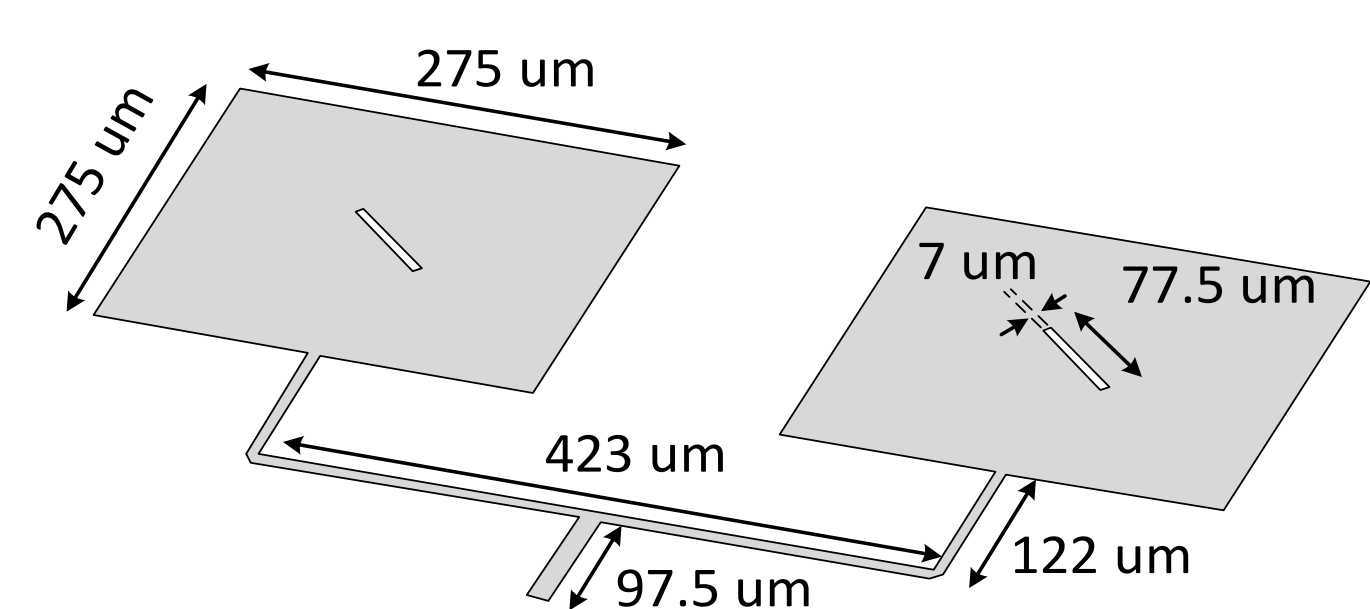


Fig. 2. The layout structure of the two-array patch antenna.

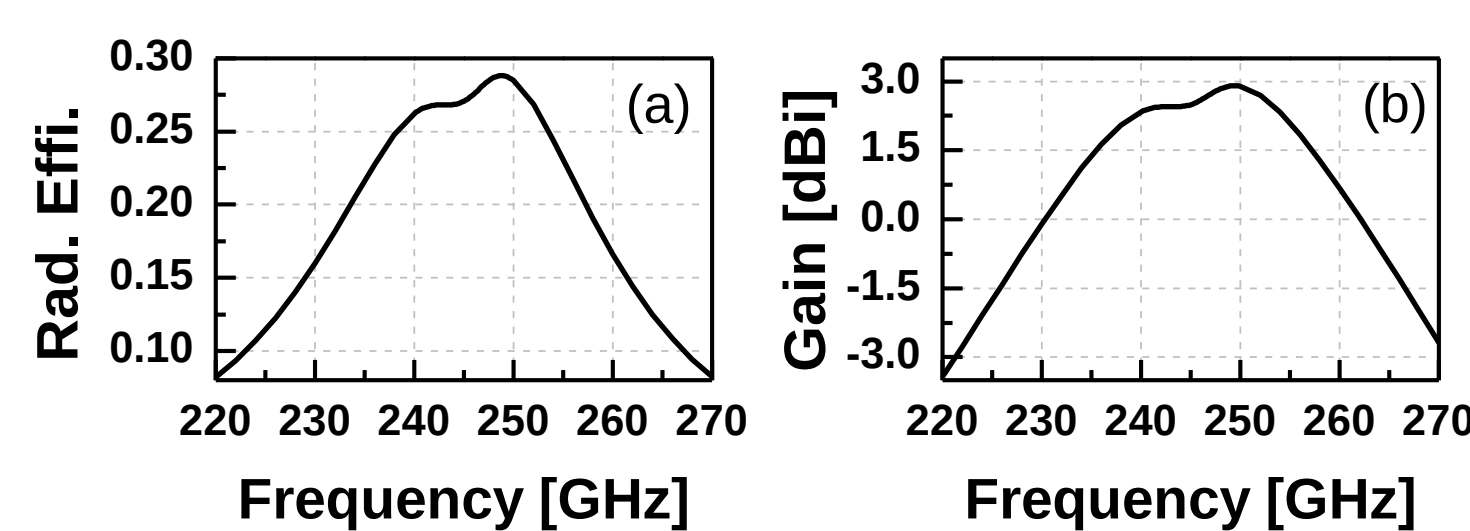


Fig. 3. The simulated (a) gain and (b) radiation efficiency of the antenna.

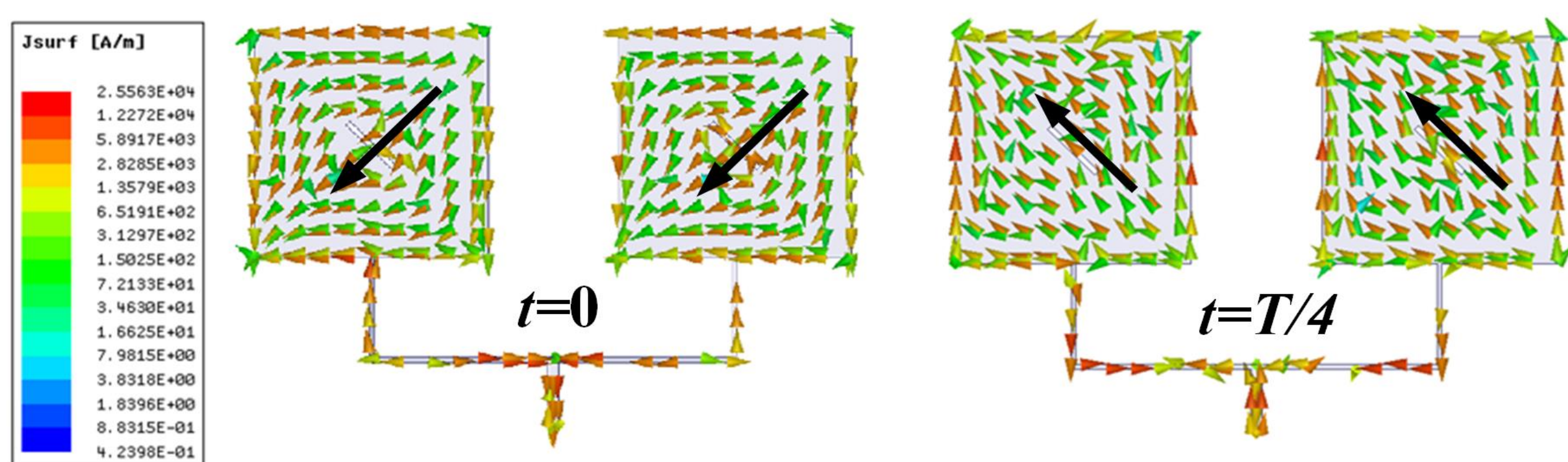


Fig. 4. Surface current density distribution of the patch antenna at  $t = 0$ , and  $t = T/4$ .

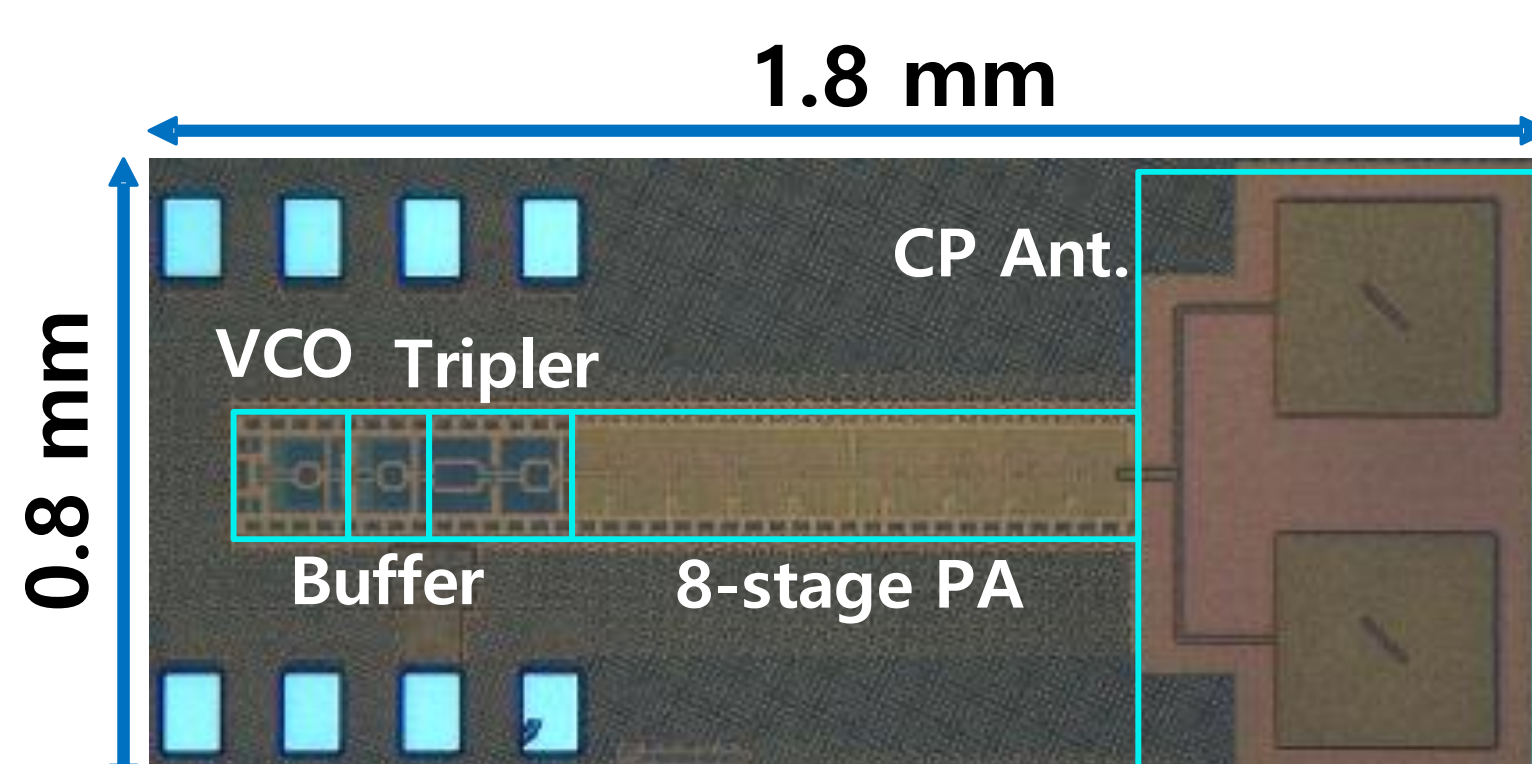


Fig. 5. A photograph of the fabricated radiator.

## Acknowledgements

This work was supported in part by the National Research Foundation of Korea grant funded by the Korea Government (MSIP) under Grant 2018R1C1B5045481, and in part by the IC Design Education Center for the chip fabrication and EDA tool.

## Measurement results

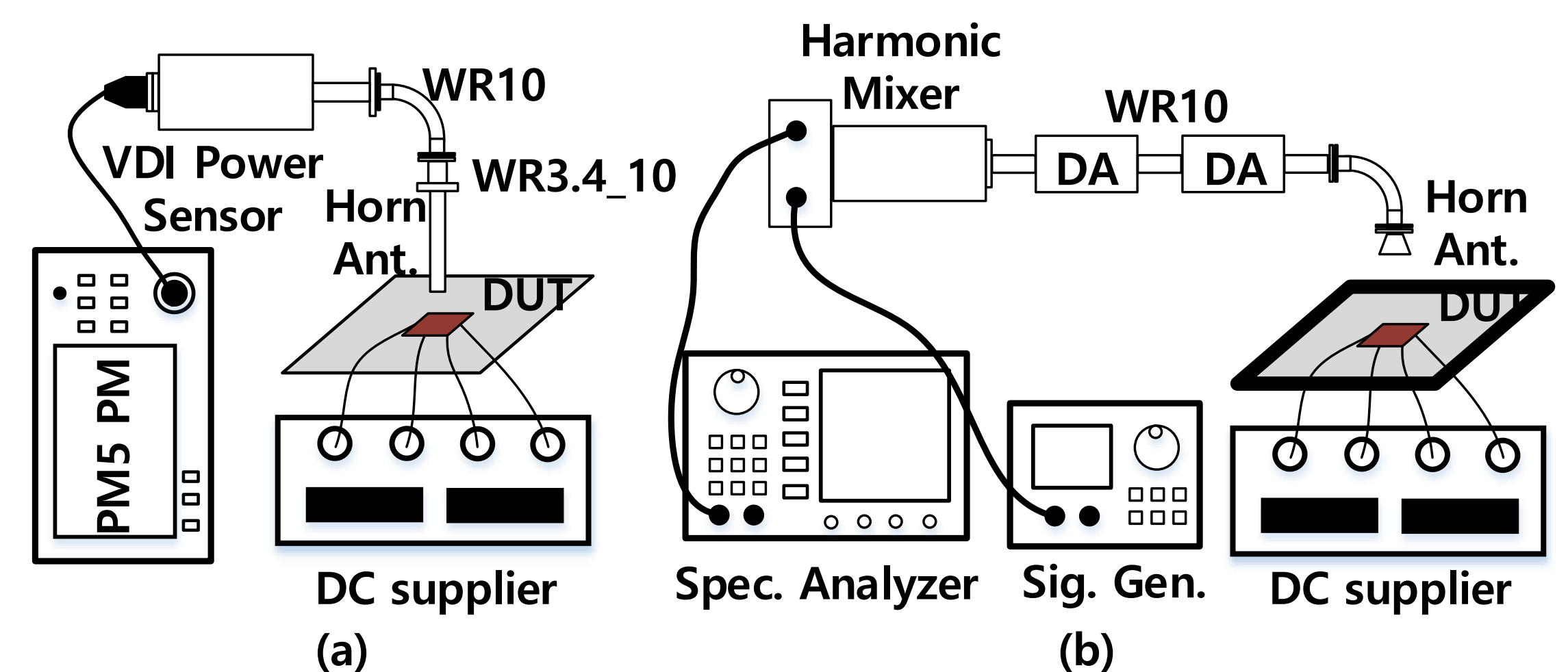


Fig. 6. Measurement setup of (a) EIRP and (b) the working frequency.

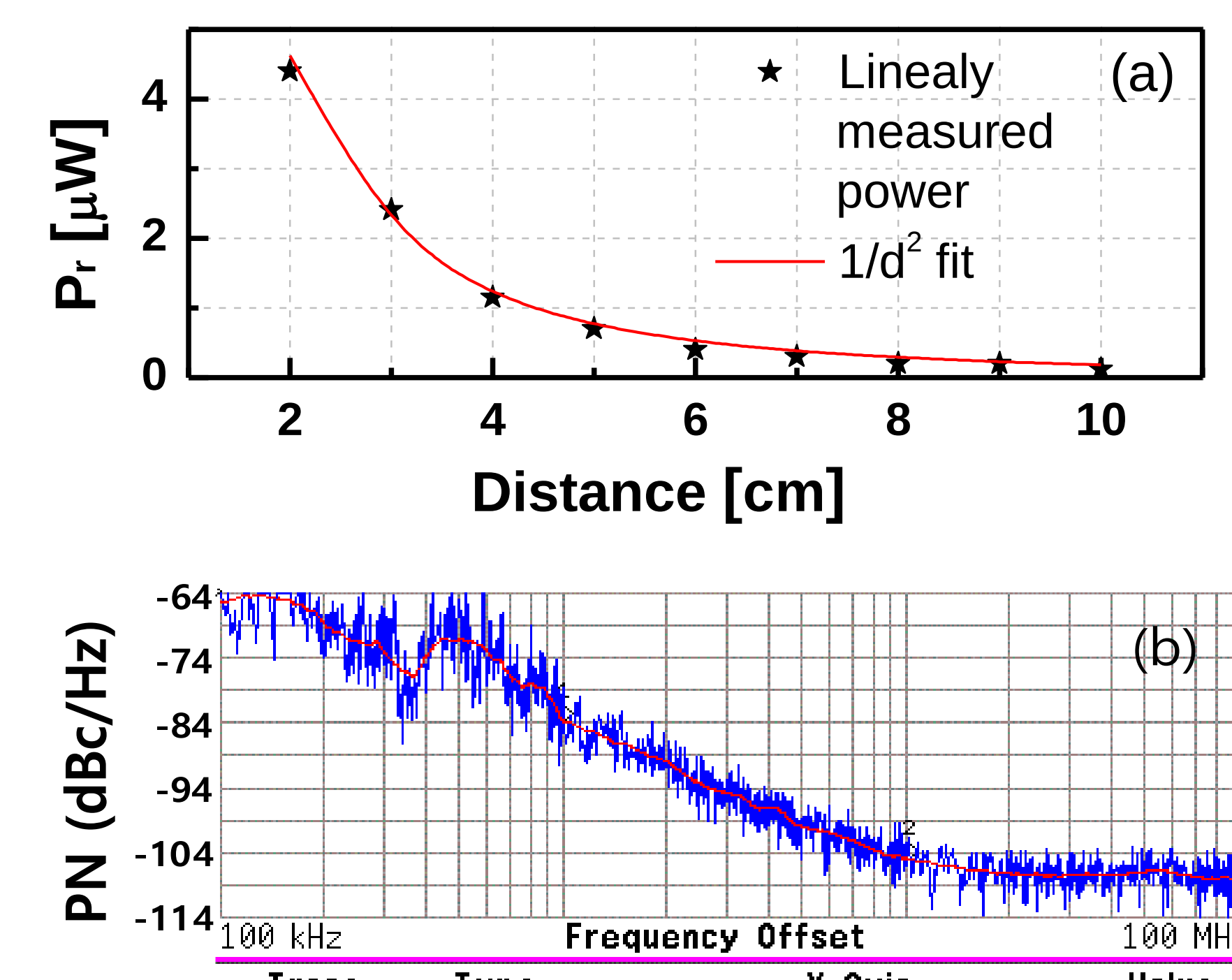


Fig. 7. (a) Received power ( $P_r$ ) of the horn antenna versus distance and (b) the measured phase noise of the received W-band leakage.

## Comparison table

| Ref.                    | [1]           | [2]                | [3]           | [4]         | [5]                | [6]           | [7]          | [8]                   | [9]              | [10]                | Ours               |
|-------------------------|---------------|--------------------|---------------|-------------|--------------------|---------------|--------------|-----------------------|------------------|---------------------|--------------------|
| Tech.                   | 130-nm SiGe   | 45-nm SOI CMOS     | 65-nm CMOS    | 130-nm SiGe | 130-nm SiGe        | 65-nm CMOS    | 130-nm SiGe  | 65-nm CMOS            | 130nm SiGe       | 130nm SiGe          | 65-nm CMOS         |
| Polar.                  | Linear        | Linear             | Linear        | Linear      | Linear             | Linear        | Linear       | Linear                | Linear           | Linear              | Circ.              |
| Array size              | 2x2           | 1x8                | 4x4           | 1x4         | 4x4                | 8 ant.        | 1x4          | 1 ant.                | 1 ant.           | 1 ant.              | 1 ant.             |
| Structure               | Couple d Osc. | Ext. Power + Mult. | Couple d Osc. | PLL + Mult. | Coupled Osc. + PLL | Couple d Osc. | SWO Scalin g | IL VCO + Tripler + PA | Mult.+ Mixer+ PA | VCO + PA + Double r | VCO + Tripler + PA |
| Freq. (GHz)             | 344           | 390                | 338           | 320         | 317                | 260           | 342          | 240                   | 240              | 245                 | 239.2              |
| $P_{rad}$ (dBm)         | -6.8          | -7                 | -0.9          | NA          | 5.2                | 0.5           | -10.5        | -0.5                  | -0.8             | 7                   | 0.5                |
| EIRP (dBm)              | 4.9           | 8                  | 17            | 10.6        | 22.5               | 15.7          | 1.2          | 1                     | 13.2             | 18                  | 3                  |
| $P_{rad}/unit$ (dBm)    | -12.8         | -16                | -12.9         | NA          | -6.8               | -8.5          | -16.5        | -0.5                  | -0.8             | 1                   | 0.5                |
| PN@10MHz (dBc/Hz)       | -93.1         | NA                 | -93           | -89         | -79                | -78.3         | -98.2        | -113.4                | NA               | -85                 | -95.2              |
| $P_{DC}$ (mW)           | 450           | 1500               | 1540          | 1000        | 610                | 800           | 425          | 220                   | 375              | 1500                | 272                |
| Area (mm <sup>2</sup> ) | 1.2           | 10.5               | 3.9           | 34.4        | 2.1                | 2.3           | 1.33         | 2                     | 1.25             | 19.98               | 1.44               |

- H. Jalili, and O. Momeni, "A 0.34-THz wideband wide-angle 2-D steering phased array in 0.13- $\mu$ m SiGe BiCMOS," *IEEE J. Solid-State Circuits*, vol. 54, no. 9, pp. 2449–2451, Sep. 2019.
- Y. Yang, O. D. Gurbuz, and G. M. Rebeiz, "An eight-element 370–410-GHz phased-array transmitter in 45-nm CMOS SOI with peak EIRP of 8–8.5 dBm," *IEEE Trans. Microw. Theory Techn.*, vol. 64, no. 12, pp. 4241–4249, Dec. 2016.
- Y. Tousi and E. Afshari, "A high-power and scalable 2-D phased array for terahertz CMOS integrated systems," *IEEE J. Solid-State Circuits*, vol. 50, no. 2, pp. 597–609, Feb. 2015.
- X.-D. Deng, Y. Li, J. Li, C. Liu, W. Wu, and Y.-Z. Xiong, "A 320-GHz 1×4 fully integrated phased array transmitter using 0.13- $\mu$ m SiGe BiCMOS technology," *IEEE Trans. THz Sci. Technol.*, vol. 5, no. 6, pp. 930–940, Nov. 2015.
- R. Han et al., "A SiGe terahertz heterodyne imaging transmitter with 3.3 mW radiated power and fully-integrated phase-locked loop," *IEEE J. Solid-State Circuits*, vol. 50, no. 12, pp. 2935–2947, Dec. 2015.
- R. Han, and E. Afshari, "A CMOS high-power broadband 260-GHz radiator array for spectroscopy," *IEEE J. Solid-State Circuits*, vol. 48, no. 12, pp. 3090–4004, Dec. 2013.
- H. Jalili and O. Momeni, "A standing-wave architecture for scalable and wideband millimeter-wave and terahertz coherent radiator arrays," *IEEE Trans. Microw. Theory Techn.*, vol. 66, no. 3, pp. 1597–1609, Mar. 2018.
- S. Kang, S. V. Thyagarajan, A. M. Niknejad, "A 240 GHz fully integrated wideband QPSK transmitter in 65 nm CMOS" *IEEE J. Solid-State Circuits*, vol. 50, no. 10, pp. 2256–2267, Dec. 2015.
- M. H. Eissa, A. Malignaggi, R. Wang, M. Elkhoully, Klaus Schmalz, A. C. Ulusoy, and D. Kissinger, "Wideband 240-GHz Transmitter and Receiver in BiCMOS Technology With 25-Gbit/s Data Rate," *IEEE J. Solid-State Circuits*, vol. 53, no. 9, pp. 2532–2542, Sep. 2018.
- K. Schmalz, J. Borngräber, W. Debski, M. Elkhoully, R. Wang, P. F.-X. Neumaier, D. Kissinger, and H.-W. Hübers, "245-GHz Transmitter Array in SiGe BiCMOS for Gas Spectroscopy," *IEEE Trans. THz Sci. Technol.*, vol. 6, no. 2, pp. 318–327, March 2016.